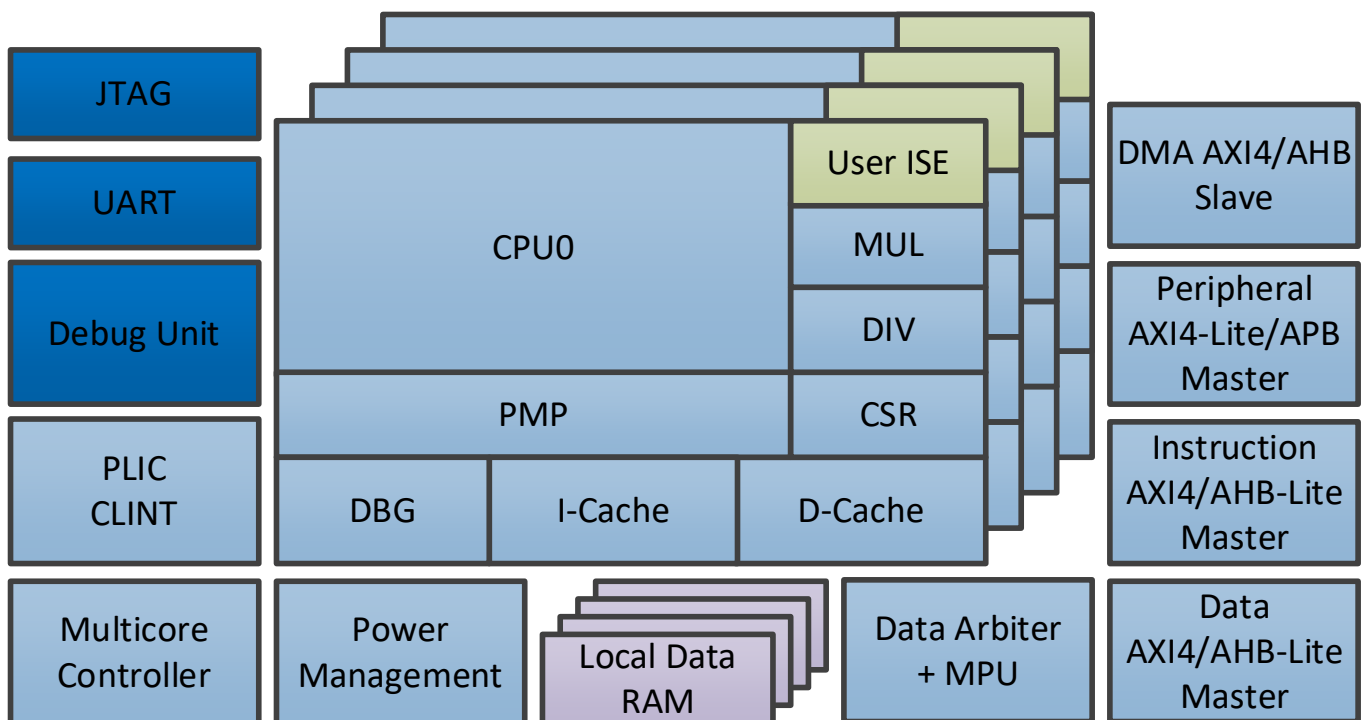


32-bit Low-Power CCRV32LP-C Processor Core

Features

- Low-Power 32-bit RISC CPU
- Simple 1-stage design
- RV32IMA instruction set
- Single or multicore implementation
- Up to 0.30 DMIPS/MHz/Core
- Up to 0.51 CoreMark/MHz/Core
- Power-down mode
- Set-associative caches with data snooping and parity
- On-chip debug support
- Separate or joint Instruction/Data interface
- JTAG and UART Debug Link
- Free GNU-Based C/GDB toolchain
- Cycle accurate simulator
- Synthesizable RTL Verilog source code



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