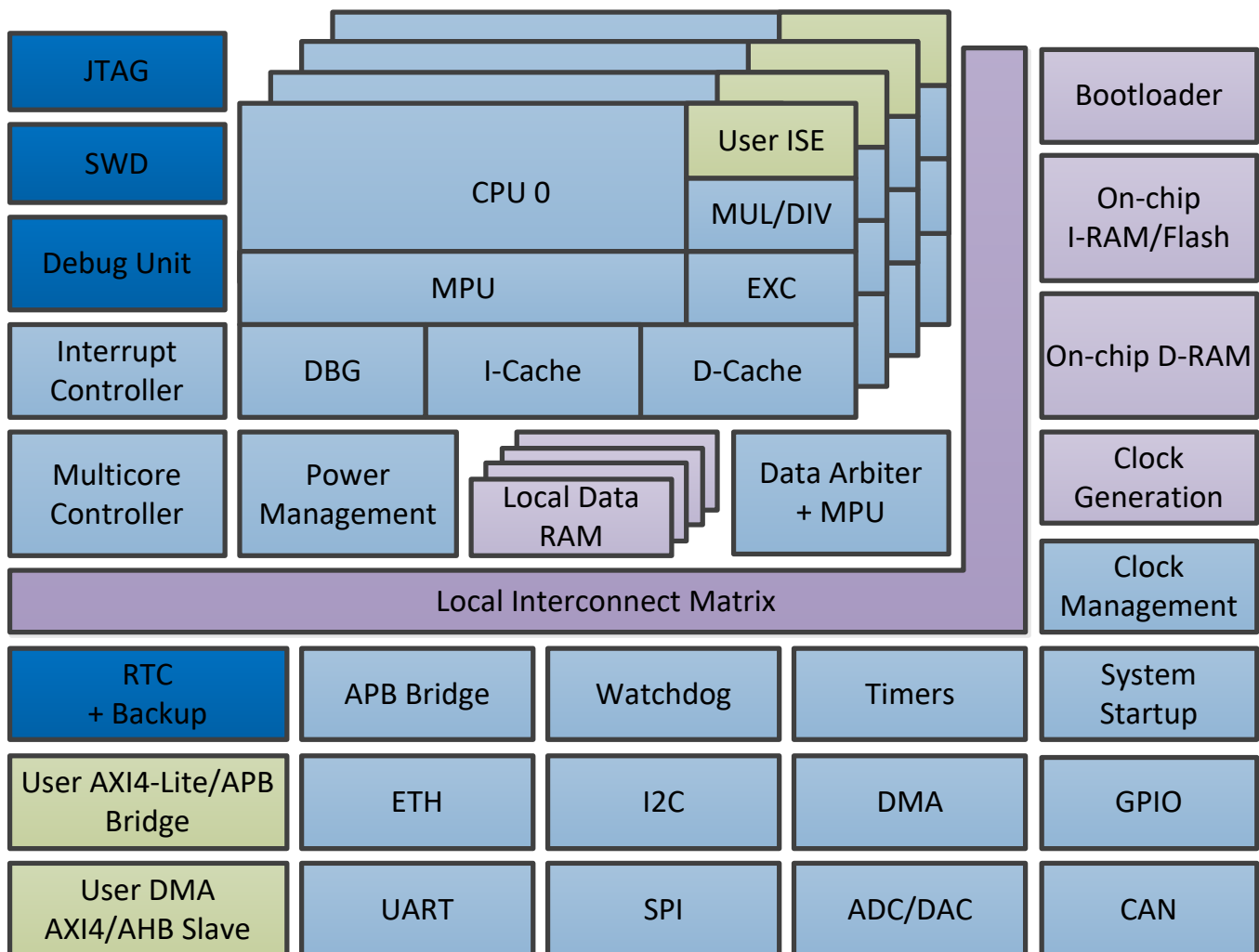


32-bit High Performance CC100-S System-on-Chip

Features

- High Performance 32-bit RISC SoC with CC100-C Processor Core
- Based on MIPS[®]-II* instruction set
- Single or multicore implementation
- Custom instruction set extension
- FPGA and silicon proven
- Power-down mode
- On-chip debug support
- Free GNU-Based C/GDB toolchain
- Cycle accurate simulator
- Synthesizable RTL Verilog source code



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